

## Electrostatic Discharged Protection Devices (ESD) Data Sheet

### Description

Brightking's LBT23C12L02 component is designed to protect sensitive electronics from damage or latch-up due to ESD and other voltage induced transient events. It is designed for use in applications where board space is at a premium. The device will protect up to two lines. It is bidirectional devices and may be used on lines where the signal polarities are above ground. TVS diodes are solid state device designed specifically for transient suppression. It feature large cross-sectional area junctions for conduction high transient currents. It offer desirable characteristics for board level protection including fast response time, low and clamping voltage, and no device degradation. The device may be used to meet the immunity requirements of IEC61000-4-2, level 4.

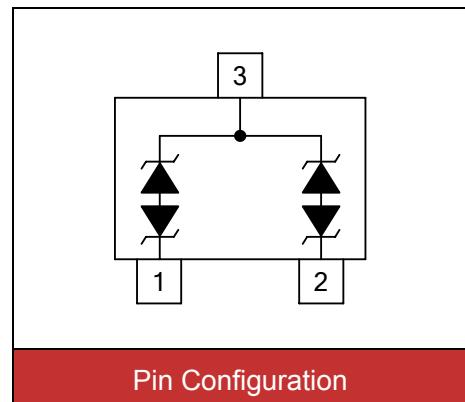


Contact : ±30kV  
Air : ±30kV



### Features

- IEC61000-4-2 ESD ±30KV Air, ±30KV contact compliance
- SOT-23 surface mount package
- Protects bidirectional two I/O lines
- Peak power dissipation of 100W under 8/20μs waveform
- Working voltage: 12V
- Low leakage current
- Low operating and clamping voltages
- Solid-state silicon avalanche technology
- Lead Free/RoHS compliant
- Solder reflow temperature: Pure Tin-Sn, 260~270°C
- Flammability rating UL 94V-0
- Meets MSL level 1, per J-STD-020
- Marking: B L12



Pin Configuration

### Applications

- Keypads, data lines, buttons
- Portable handheld devices
- Portable GPS
- Notebook computers
- Digital Cameras
- MP3 players

### Maximum Ratings

Rating	Symbol	Value	Unit
Peak pulse power (tp=8/20μs waveform)	P <sub>PP</sub>	100	W
Peak pulse current (tp=8/20μs waveform)	I <sub>PP</sub>	2.5	A
ESD voltage (Contact discharge)	V <sub>ESD</sub>	±30	kV
ESD voltage (Air discharge)		±30	
Storage & operating temperature range	T <sub>STG</sub> , T <sub>J</sub>	-55~+150	°C

## Electrical Characteristics ( $T_J=25^\circ\text{C}$ )

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Reverse stand-off voltage	$V_{RWM}$				12	V
Reverse breakdown voltage	$V_{BR}$	$I_{BR}=1\text{mA}$	13.3			V
Reverse leakage current	$I_R$	$V_R=12\text{V}$ Each I/O pin			1	$\mu\text{A}$
Clamping voltage ( $t_p=8/20\mu\text{s}$ )	$V_C$	$I_{PP}=1\text{A}$			25	V
Clamping voltage ( $t_p=8/20\mu\text{s}$ )	$V_C$	$I_{PP}=2.5\text{A}$			30	V
Off state junction capacitance	$C_J$	0Vdc, $f=1\text{MHz}$ Between I/O pins and GND		8	15	pF

## Typical Characteristics Curves

Figure 1. Power Derating Curve

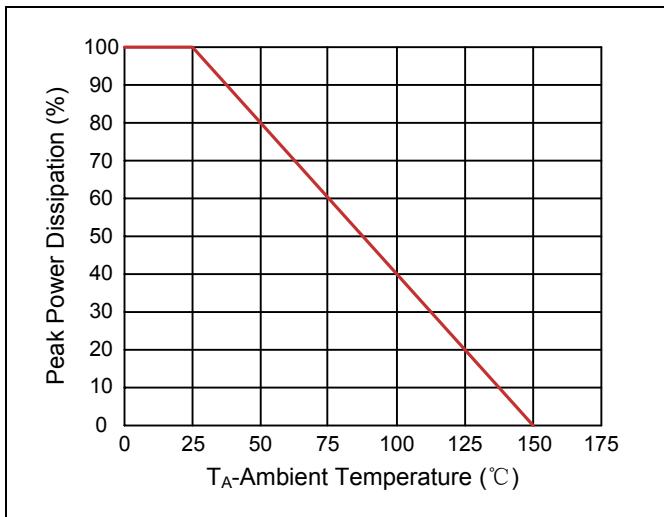


Figure 2. Pulse Waveforms

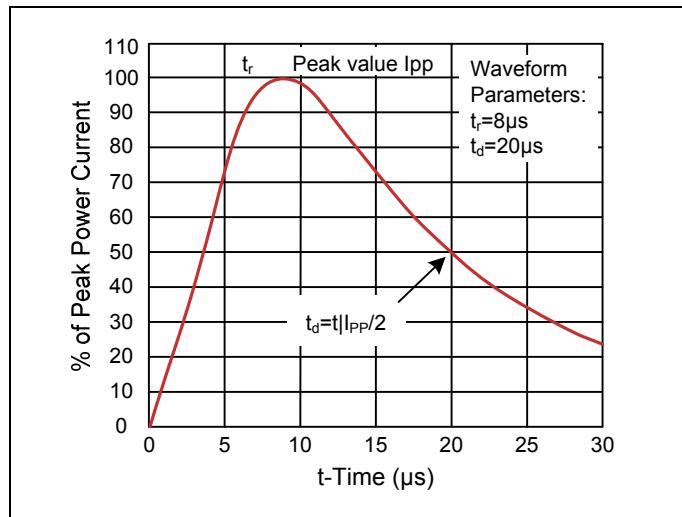


Figure 3. Clamping Voltage vs. Peak Pulse Current

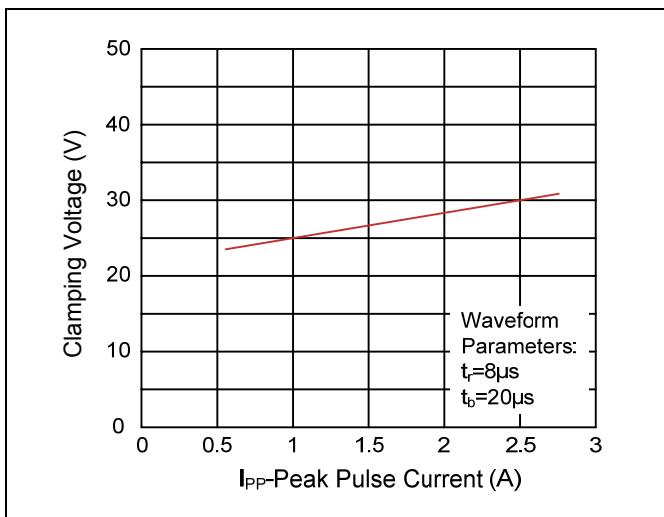
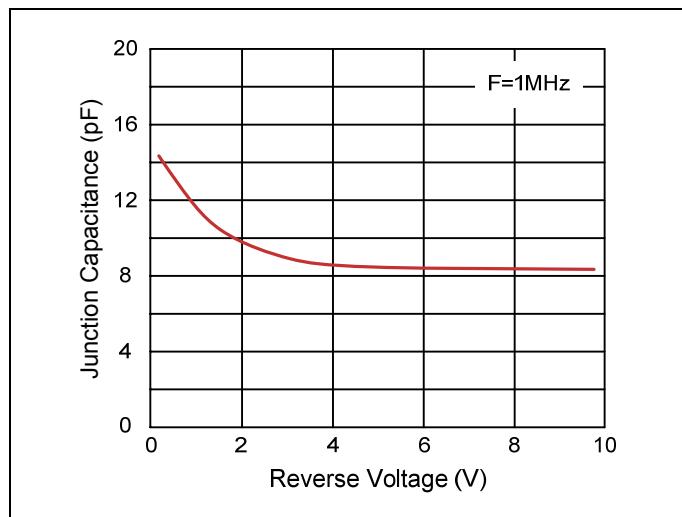
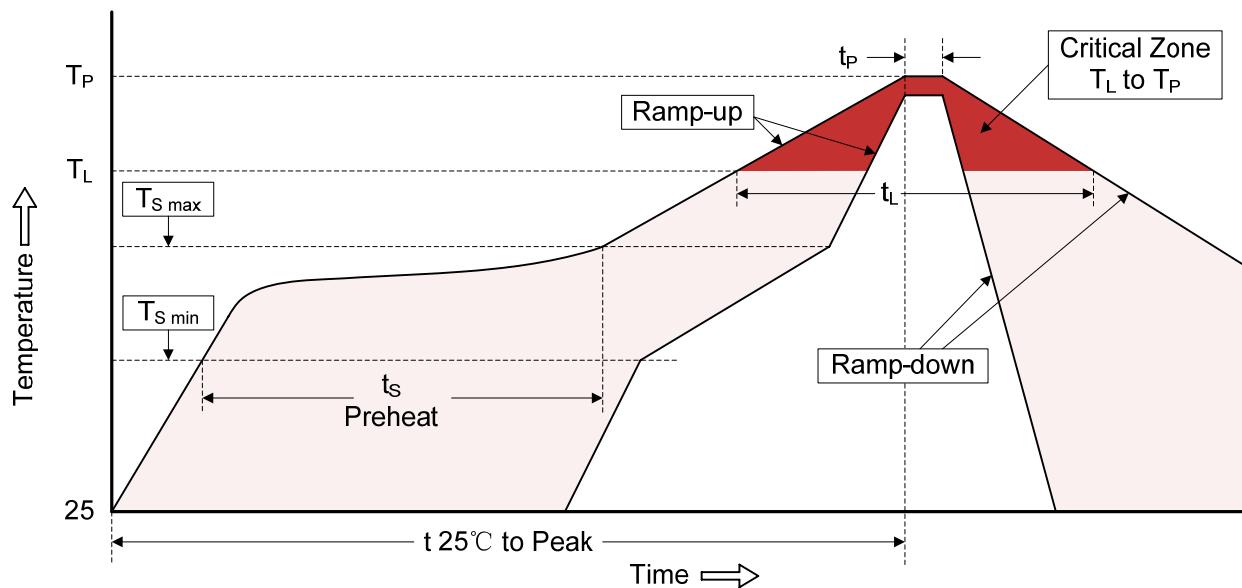


Figure 4. Normalized Capacitance vs. Reverse Voltage



## Recommended Soldering Conditions

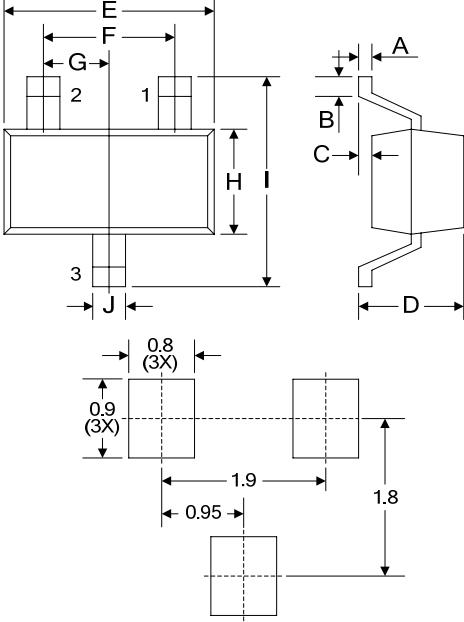
### Reflow Soldering



### Recommended Conditions

Profile Feature	Pb-Free Assembly
Average ramp-up rate ( $T_L$ to $T_P$ )	3°C/second max.
Preheat	
-Temperature Min ( $T_{S \min}$ )	150°C
-Temperature Max ( $T_{S \max}$ )	200°C
-Time (min to max) ( $t_s$ )	60-180 seconds
$T_{S \max}$ to $T_L$	
-Ramp-up Rate	3°C/second max.
Time maintained above:	
-Temperature ( $T_L$ )	217°C
-Time ( $t_L$ )	60-150 seconds
Peak Temperature ( $T_P$ )	260°C
Time within 5°C of actual Peak Temperature ( $t_P$ )	20-40 seconds
Ramp-down Rate	6°C/second max.
Time 25°C to Peak Temperature	8 minutes max.

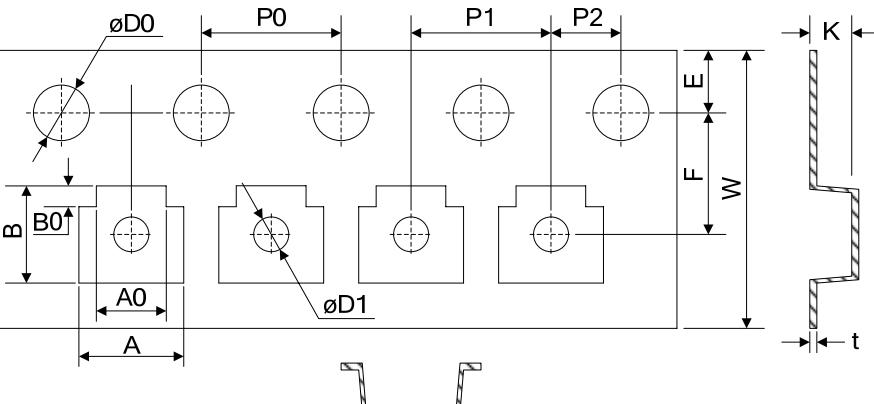
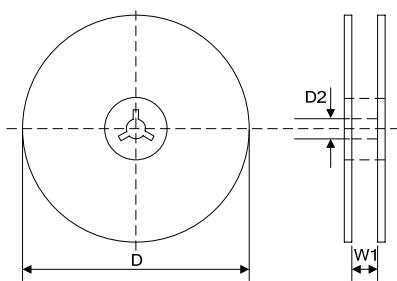
Dimensions (SOT-23)



The technical drawing shows the physical dimensions of the SOT-23 package. It includes top and side views with labeled dimensions A through J. Below the main drawing is a detailed 'Recommended Soldering Pad Layout' showing the internal structure of the package with pads labeled 1, 2, and 3.

Symbol	Dimension			
	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	0.08	0.18	0.003	0.007
B	0.15	-	0.006	-
C	-	0.13	-	0.005
D	0.89	1.09	0.035	0.043
E	2.80	3.05	0.110	0.120
F	1.90		0.075	
G	0.95		0.037	
H	1.19	1.40	0.047	0.055
I	2.10	2.49	0.083	0.098
J	0.35	0.50	0.014	0.020

Packaging

Tape	 <p>The diagram shows the layout of the tape for the SOT-23 packages. It includes dimensions for the width of the tape (W), pitch between packages (P0, P1, P2), lead diameter (D0, D1), lead height (E, F), lead thickness (t), and various other structural details like A, A0, B, B0, and K.</p>	<table border="1"> <thead> <tr> <th>Symbol</th> <th>Dimension (mm)</th> </tr> </thead> <tbody> <tr> <td>W</td> <td>8.00±0.30</td> </tr> <tr> <td>P0</td> <td>4.00±0.10</td> </tr> <tr> <td>P1</td> <td>4.00±0.10</td> </tr> <tr> <td>P2</td> <td>2.00±0.10</td> </tr> <tr> <td>D0</td> <td>Φ1.55±0.10</td> </tr> <tr> <td>D1</td> <td>Φ1.00±0.05</td> </tr> <tr> <td>E</td> <td>1.75±0.10</td> </tr> <tr> <td>F</td> <td>3.50±0.10</td> </tr> <tr> <td>A</td> <td>3.10±0.10</td> </tr> <tr> <td>A0</td> <td>2.10±0.10</td> </tr> <tr> <td>B</td> <td>2.75±0.10</td> </tr> <tr> <td>B0</td> <td>0.65±0.10</td> </tr> <tr> <td>K</td> <td>1.10±0.10</td> </tr> <tr> <td>t</td> <td>0.20±0.05</td> </tr> </tbody> </table>	Symbol	Dimension (mm)	W	8.00±0.30	P0	4.00±0.10	P1	4.00±0.10	P2	2.00±0.10	D0	Φ1.55±0.10	D1	Φ1.00±0.05	E	1.75±0.10	F	3.50±0.10	A	3.10±0.10	A0	2.10±0.10	B	2.75±0.10	B0	0.65±0.10	K	1.10±0.10	t	0.20±0.05
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